

하드웨어 기능을 활용한 임베디드 시스템 보안 연구

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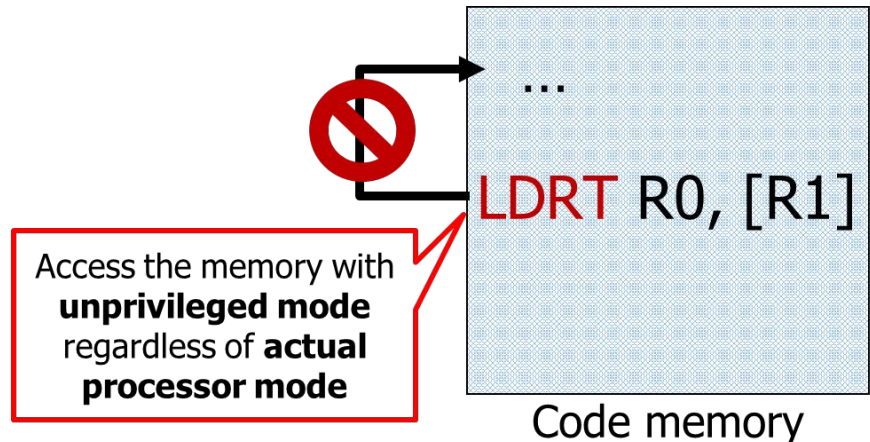
But, in low-end embedded systems..

- Constrained computing resources
 - Low cost, low power consumption
 - Small memory, Low CPU clock speeds
- Lack of processor architectural supports
 - No MMU (no virtualization)
 - Few security extensions



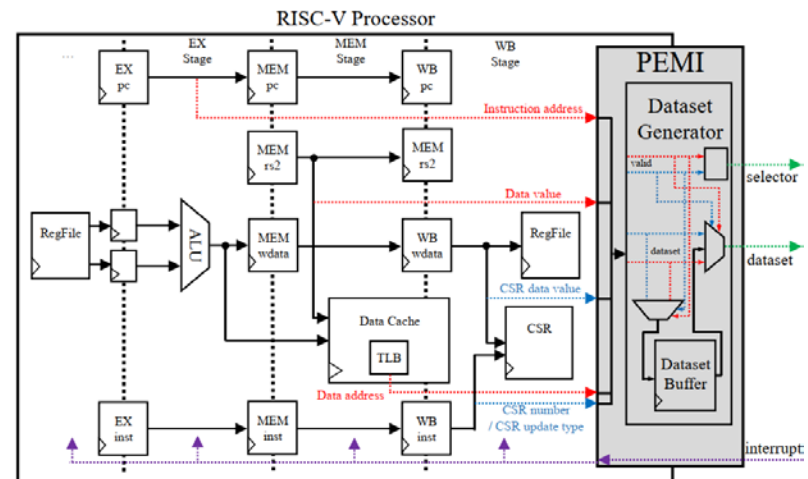
1. Use Existing Hardware Features

- uXOM: Efficient eXecute-Only Memory on ARM Cortex-M
 - Presented at USENIX Security '19
 - eXecute-Only-Memory (XOM)
 - Use as a primitive for many security solutions
 - Already provided in high-end processor architecture
 - uXOM
 - Use unprivileged memory instructions (LDRT, STRT) in ARMv7-M
- Other features
 - ARM TrustZone-M



2. Add New Hardware Features

- RiskiM: Toward Complete Kernel Protection with Hardware Support
 - Presented at Design Automation and Test in Europe (DATE) '19
 - Integrity Monitor
 - Monitor various system behaviors
 - Memory events, system configuration, executed instructions ...
 - Add special hardware interface for this
- Next step
 - Add special instructions for security



Conclusion

- Embedded system security
 - Using existing hardware features
 - Adding new hardware features
- There are opportunities
 - Numerous use cases
 - RISC-V open source ISA
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